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Application Number	10/657,196	_		
Confirmation Number .	1621			
Filing Date	September 09, 2003			
First Named Inventor	Kenji SERA	_		
Art Unit	1621			
Examiner Name	Unknown	_		
Attorney Docket Number	Q77403			

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Examiner	Cite	Document 1		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document			
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-ESP		K. Kagaku Shuppan, "Basics of MOS Integrated Circuits, No. 5 of VLSI Introduction Series", pp. 62-65 with Partial translation.				
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